



Application No. 09/522,470
Art Unit: 2193

Amendment under 37 C.F.R. §1.116
Attorney Docket No.: 000267

AMENDMENTS TO THE CLAIMS

The listing of claims below replaces all prior versions of claims in the application.

1. – 6. (Cancelled)

7. (Previously Presented) A logic circuit, comprising:

a first inversion section for inverting a first input signal and outputting the inverted signal;

a second inversion section for inverting the inverted signal of the first input signal and outputting a resulting signal;

a first outputting section for performing NANDing arithmetic between the output of said first inversion section and a second input signal and outputting a first resulting signal; and

a second outputting section for performing NANDing arithmetic between the output of said second inversion section and an inverted signal of the second input signal and outputting a second resulting signal;

said first outputting section and said second outputting section being switched with the second input signal and the inverted signal of the second input signal, said first outputting section outputs the first resulting signal and said second outputting section outputs the second resulting signal.

8. – 18. (Cancelled)